PARTICIPANTS:

Faculty from AICTE Institutions / Universities working in related fields are eligible. Participants should bring a letter of nomination from Head of the Institution stating that they are being deputed for the course. The selection is based on first cum first serve basis.

HOW TO APPLY:

Interested participants should download and submit the complete application form in prescribed format given, which is available online and send to coordinators mailing address. Filled in application form should be accompanied by a Demand Draft drawn in favor of "The Principal, Narayana Engineering College, Nellore ", payable at Nellore. The registration fee includes the course material, lunch and refreshments.

Registration Fees: Rs. 500/-
Last Date for Registration: 25.06.2025

Resource Persons:

Experts from industry and prestigious academic institutions.

Travel and Accommodation:

All the out station participants will be provided accommodation on payment basis in the Guest House of the Institute, subjected to availability.

For More Information

Dr. K. Murali, Professor & HOD
Mobile: +91-9441818312
Email: hodece@necn.ac.in

Dr. K S Sagar Reddy, Professor, ECE
(Mobile : 9704803650)

Dr.P Giri Prasad, Assoc. Professor, ECE
(Mobile : 6309877148)

CHIEF PATRON

Mr.K. Puneeth, President
Narayana Group of Educational Institutions.

PATRONS

Dr. B. Dattatreya Sarma, Director

Dr. G. Srinivasulu Reddy, Principal

Convenor

Dr K. Murali, HOD, Dept. of ECE

Coordinators

Dr. K. S. Sagar Reddy, Professor, ECE

Dr. P. Giri Prasad, Assoc. Professor, ECE

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Dr. E. Vijaya Lakshmi, Professor, ECE

Mr. P. Sravan Kumar Reddy, Associate Professor, ECE

Mr. V. Sudheer, Associate Professor, ECE

Mr. B. Sukumar, Associate Professor, ECE

Mrs. Syed. Athika Sultana, Assistant Professor, ECE

Mrs. A. Vidyullatha, Assistant Professor, ECE

Mrs. I. Divya, Assistant Professor, ECE

Mr. V. Praveen, Assistant Professor, ECE

Mrs. J. K. Neelima Bai, Assistant Professor, ECE

Mrs. S. Deepa Rani, Assistant Professor, ECE

Mrs. R. Prashanthi, Assistant Professor, ECE

Advisory Committee

Dr. K. Viswak Sena Reddy, Dean Planning and Development

Dr. A. V. S . Sreedhar Kumar, Vice Principal & HoD, MECH

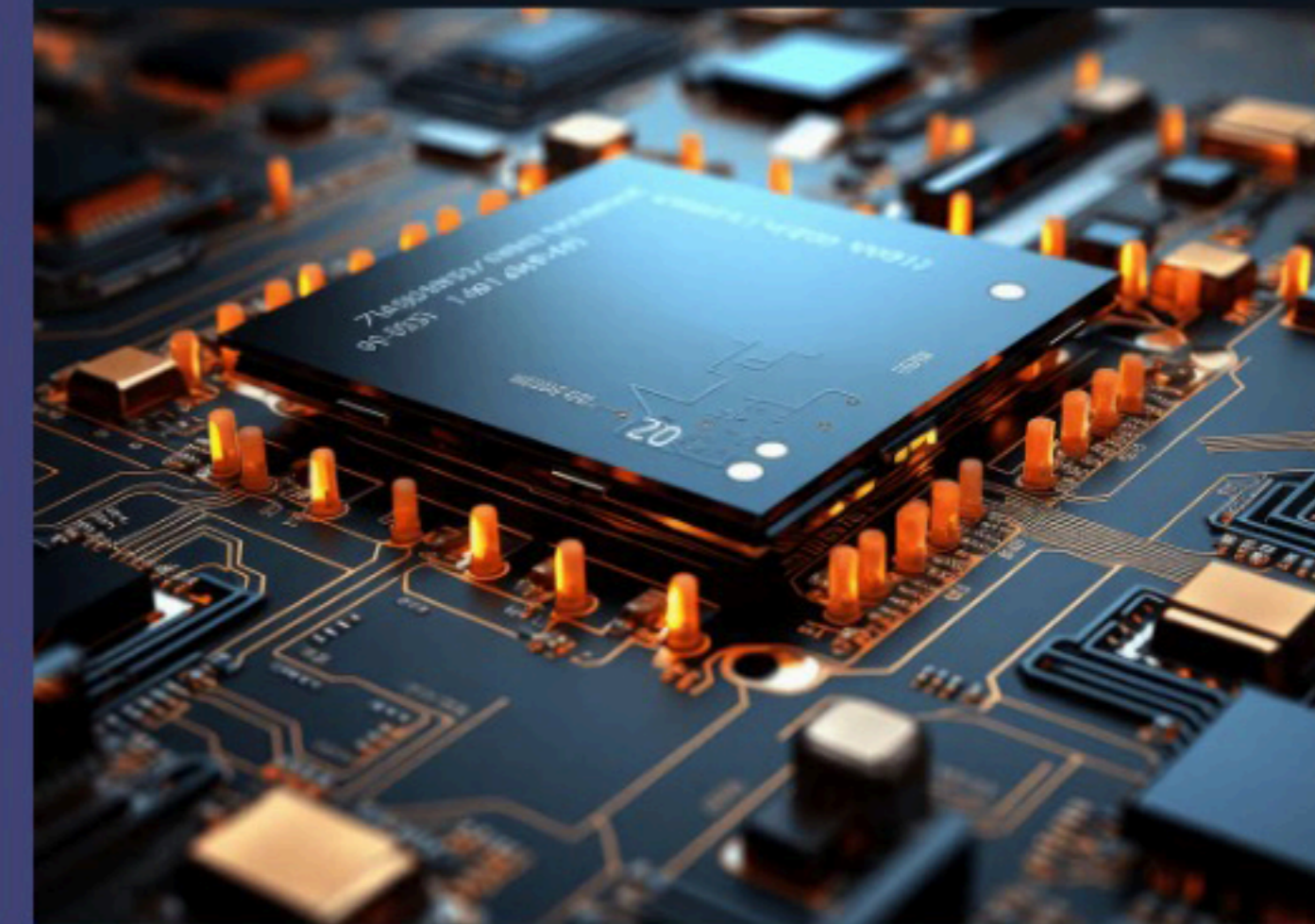**Dr. A.Hazarathaiah,** Vice Principal (Academics)

Dr. C. Rajendra, HOD, CSE

Dr. G. Venkatesrarlu, HOD, EEE

Dr. K Venkata Lakshmi, HOD, CIVIL

NARAYANA
ENGINEERING COLLEGE
(AUTONOMOUS) NELLORE

27 Years of Excellence

Dr. P. NARAYANA FOUNDER

A Five Day FDP on
"AI-Powered VLSI Design: A Paradigm Shift in Chip Engineering"

(JUNE 28th to JULY 3rd, 2025)

Organized by

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

ABOUT THE INSTITUTE:

Narayana Engineering College (NECN) is one of the premier engineering college in Andhra Pradesh sponsored by Narayana group of Educational Institutions. NECN aims to expand the goal of education for building students' character, creating a well-rounded individual possessing key skills with higher emphasis on critical thinking and holistic learning.

NECN, over the past 27 years has become a shrine of knowledge and shaped thousands of famous and adroit graduates and post graduates, who became successful in their careers, serving all over the world.

The credentials include placement of 11000+ students till date since the inception of the college. NECN has bagged more than 33 gold medals from JNTU, Ananthapuramu and 16 Prathibha awards from the Govt.of A.P till now .

Some of the unique features are, NECN has full Autonomy, accredited with National Assessment and Accreditation Council (NAAC), National Board of Accreditation (NBA) for CSE, ECE, and EEE for three years, Ranked Grade 'A' by Government of Andhra Pradesh, Recognized by UGC 2(f) and 12(B), and Certified by ISO 9001:2015, ISO 14001:2015, ISO 50001:2018 and permanently affiliated to JNTUA, Ananthapuramu.

The College has renowned brand and students take pride in associating themselves with the institution.

ABOUT THE DEPARTMENT:

The aim of the department is to produce Electronics & Communication Engineers, who combine academic excellence with practical expertise. The department of Electronics and Communication Engineering came into existence at Narayana engineering college in 1998 by the approval of the All India Council for Technical Education (AICTE). The department has progressed rapidly and is now accredited with, National Board of Accreditation (NBA) for three years with excellent infrastructure and a highly qualified and dedicated faculty. At present, the department is offering B.Tech (ECE), B.Tech Electronics and Communication (Advanced Communication Technology), B.Tech Electronics Engineering (VLSI Design & Technology), and M.Tech (VLSI) with total intake of 300 and 12 respectively. The majority of our students have been well placed in many companies through campus interviews and many of our students are pursuing their higher education at various premier institutions like IIT's, NIT's and also in abroad.

The department involves in teaching and research in diverse aspects of Communications, IOT, VLSI Design, Embedded Systems, Signal Processing, Image Processing and Neural Networks. Apart from teaching, the faculty members have been vigorously pursuing research and regularly publishing research papers in various reputed National and International Journals as well as in prestigious conferences.

ABOUT FDP:

This FDP aims to provide a comprehensive introduction to AI-Powered VLSI Design, covering both foundational concepts and emerging applications. Participants will gain insights into AI-driven design automation, machine learning for design optimization, and real-world use cases in chip engineering. Designed for faculty and researchers, the program offers expert-led sessions and discussions on the future of AI-powered VLSI design, enabling them to integrate these advancements into their teaching and research.

OBJECTIVES:

1. Understand AI applications: Equip faculty members with knowledge of AI applications in VLSI design, including design automation, optimization, and analysis.
2. Learn AI-powered design techniques: Train faculty members on AI-powered design techniques, such as machine learning and reinforcement learning, for VLSI design.
3. Explore future directions: Discuss future directions and re search opportunities in AI-powered VLSI design.
4. Enhance teaching capabilities: Enable faculty members to integrate AI-powered VLSI design into their curriculum and re search.
5. Foster collaboration: Provide a platform for faculty members to network and collaborate with peers and industry experts in AI-powered VLSI design.
6. Stay updated with industry trends: Update faculty members on industry trends, challenges, and applications of AI-powered VLSI design

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(June 28th to July 3rd, 2025)

Registration Form

Name:..

Qualification:..

Designation:...

Address for Correspondence..........................

....

....

E-mail ID: ....

Mobile Number: ....

Experience (if any): ....

Place: Signature of the Applicant

Date:

                                                    Principal